

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

STEPHEN H. GUNTHER, ET AL.

Application No.:

Filed:

For: **METHODS AND APPARATUS FOR THERMAL
MANAGEMENT OF AN INTEGRATED CIRCUIT DIE**

Art Group: 2863

Examiner: Tung S. Lau

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). This IDS and IDS Citation Form are being submitted concurrently with the Divisional Application. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s). Some or all of the references listed on the enclosed PTO/SB/08 were previously identified in the parent application (Application No. 09/784,255, filed February 14, 2001) and copies of the references were furnished at that time. Accordingly, per 37 CFR §1.98(d)(1) additional copies of those references are not submitted herewith.

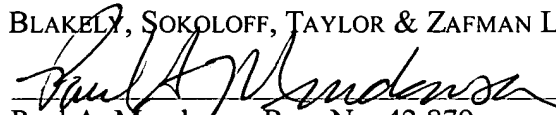
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,


BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Date: April 09, 2004


Paul A. Mendonsa, Reg. No. 42,879

12400 Wilshire Boulevard, 7th Floor
Los Angeles, CA 90025
Telephone: (503) 684-6200

I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



Tamara M. Simpson

04-09-04

Date

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known	
				Application Number	
				Filing Date	
				First Named Inventor	
				Art Unit	
				Examiner Name	
				Attorney Docket Number	
Sheet	1	of	4		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number - Kind Code ² (if known)	Publication Date or Issue Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	1	US-6,172,611	01-09-2001	Hussain et al.	
	2	US-5,675,297	10-07-1997	Gose et al.	
	3	US-5,256,914	10-26-1993	Booner, James B	
	4	US-5,713,030	01-27-1998	Evoy	
	5	US-5,838,578	11-17-1998	Pippen	
	6	US-6,137,329	10-24-2000	Kardash	
	7	US-6,336,593	01-08-2002	Bhatnagar	
	8	US-6,101,516	08-09-2000	Wolrich et al.	
	9	US-H562	12-06-1988	Trachier et al.	
	10	US-Re. 32,960	06-20-1989	Levine	
	11	US-Re. 34,789	11-15-1994	Premarlani	
	12	US-4,442,972	04-17-1984	Sahay et al.	
	13	US-4,488,824	12-18-1984	Salem	
	14	US-4,591,855	06-14-1988	Bufano, Jr. et al.	
	15	US-4,779,161	10-18-1988	DeShazo, Jr.	
	16	US-4,787,007	11-22-1988	Matsumura et al.	
	17	US-4,789,819	12-16-1988	Nelson	
	18	US-4,799,176	01-14-1989	Cacciatore	
	19	US-4,807,144	07-25-1999	Day	
	20	US-4,903,106	02-20-1990	Fukunaga et al.	

FOREIGN PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Foreign Patent Document Country Code ³ - Number ⁴ - Kind Code ⁵ (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

Examiner Signature	Date Considered
--------------------	-----------------

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Complete if Known	
				Application Number	
Sheet 4 of 4				Filing Date	April 9, 2004
				First Named Inventor	Stephen H. Gunther
				Art Unit	2863
				Examiner Name	Tung S. Lau
				Attorney Docket Number	42P4728X Q

NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
	48	SHU-YUAN CHIN & CHUNG-YU WU. " A New Type of Curvature-Compensated CMOS Bandgap Voltage References', VLSITSA, 1991, Pages 398-402.		
	49	MARCO FERRO,FRANCO SALERNO & RINALDO CASTELLO, " A Floating CMOS Bandgap Voltage References for Differential Applications". IEEE: Journal of Solid-State Circuits. Vol 24 no. 3, June 1989, Pages 690-697		
	50	O. SALMINEN, K HALONEN, "The Higher Order Temperature Compensation of Bandgap Volt References", IEEE, 1992, Pages 1388-1391		
	51	HECTOR SANCHEZ, ET AL. " Thermal Management System for High Performance PowerPC Microprocessors", IEEE, COMPCON '97, February 1997, Pages 325-330.		
	52	PHILLIP E. ALLEN & DOUGLAS R. HOLBERG, " CMOS Analog Circuit Design", HRW, Pages 529-551		
	53	" Low Power, Programmable Temperature Controller", Rev. C, Analog Devices, Inc., 1995 Pages 1-16.		

Examiner Signature		Date Considered	
--------------------	--	-----------------	--

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450